



VIP—Vision-in-package

The all-in-one vision system

Vision-in-package (VIP) provides powerful machine vision wherever you need it by combining a superior imaging front end with embedded processing in a single compact module.

Key features

The vision-in-package device contains a powerful H7 micro-controller (Cortex-M7 core), many possible imagers, fast memory and communication.

The optics and illumination can be added/adapted to the application at hand.

Technical specifications

- Adaptive platform for vision applications
- Miniature (18.5 x 18.5 x 18.5 mm)
- Low cost
- Cheaper and shorter time to market than an ASIC
- Easy to integrate and adaptive
- Easy to customize to any kind of vision processing
- Ready to accommodate NN hardware accelerators
- Available SDK on Mac / PC / Linux



A complete system for face recognition

Applications

- Low-cost retrofit product recognition
- In-Face recognition
- Industrial machine vision
- Driver assistance

CPU	Cortex-M7 STM32H743—400 MHz
Memory	2 MB Flash 0 wait states 64 MB of SDRAM—6 ns
Imager supported	Aptina MT9V024 E2V EV76C541 Other 10 bit parallel imagers possible on demand (I.e. Ulys IR-Pico384)
Sensors	3D acceleration, 3D Gyro, 3D Magneto Temperature
Host connection	2 serial, 1 50 MHz SPI, 1 JTAG (encapsulated by USB)
Additional interfaces	Bluetooth, micro SDCard, battery
Software package	CSEM µKernel + OS
Examples	Available in the package

Recognition pipeline performances (Software)

HDR CSEM imager	130 db, 320 x 240 pixels
Face detection	39 ms
Facial landmark localisation	8.5 ms
Face recognition	19.5 ms

